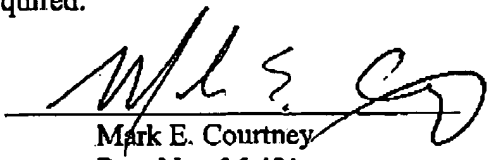


<b>AMENDMENT TRANSMITTAL LETTER</b>				Attorney Docket No: 00P09113 US		
Application Serial Number: 09/751,474		Filing Date: 12/28/2000		Examiner: Steven Loke		Group Art Unit: 2811
Invention: Area Efficient Stacking of Antifuses in Semiconductor Device						
TO THE COMMISSIONER OF PATENTS AND TRADEMARKS: Transmitted herewith is an amendment in the above-identified application. The fee has been calculated as shown below.						
<b>CLAIMS AS AMENDED</b>						
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	NO. OF EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	26	MINUS	27	0	\$50	\$ 0.00
INDEP. CLAIMS	6	MINUS	6	0	\$200	\$ 0.00
_____ Petition is hereby made under 37 CFR 1.136(a) to extend the time for response to the Office Action of _____ to and through _____, comprising an extension of the shortened statutory period of: <div style="display: flex; justify-content: space-around; margin-top: 10px;"> <div>           _____ one month (\$110)            _____ two months (\$400)         </div> <div>           _____ three months (\$920)            _____ four months (\$1,440)         </div> </div>						
<b>TOTAL ADDITIONAL FEE FOR THIS AMENDMENT</b>						<b>\$ 0.00</b>
_____ Applicant claims small entity status. See 37 CFR 1.27. Therefore the fee amount shown above is reduced by one-half, and the resulting fee is: \$ _____. _____ A check in the amount of \$ _____ is attached. _____ Charge \$ _____ to Deposit Account <u>50-1065</u> . <u>X</u> Charge any additional fees or credit any overpayments to Deposit Account <u>50-1065</u> . <u>X</u> No additional fee is required. <div style="display: flex; justify-content: space-between; margin-top: 20px;"> <div> <u>April 11, 2005</u> </div> <div style="text-align: center;">               Mark E. Courtney              Reg. No. 36,491           </div> </div>						

APR 11 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lehmann, *et al.* Docket No.: 00P09113 US  
Serial No.: 09/751,474 Art Unit: 2811  
Filed: December 28, 2000 Examiner: Loke, Steven  
Title: Area Efficient Stacking of Antifuses in Semiconductor Device

Mail Stop: Amendment  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

The following amendments and remarks are presented in response to the Examiner's Office Action mailed January 11, 2005. They are believed to be a full and complete response and to place this application in a condition for allowance. Reconsideration is requested. Please amend the above-referenced application as follows.